

09/999543

- 44 -

Abstract of the Disclosure

A hardware description verifying system includes a storage unit, an output unit and a processor. The storage unit stores a source program 5 for hardware description in a program language. The processor detect a portion of the source program different in logic interpretation between a case of compiling the source program using a compiler and a case of behavioral synthesis, and outputs existence of 10 the source program portion to the output unit.